

ABSTRACT OF DISCLOSURE

Pixels of a liquid crystal display are laid on a delta pattern, and short-circuit is liable to take place between a source layer and a drain layer and/or between a gate layer and a storage electrode layer, wherein a contact slit is formed in a gate insulating layer intervening between the gate/storage electrode layers and the source/drain layers in such a manner as to break a piece of residual amorphous silicon and make a piece of residual metal exposed thereto, and the piece of residual metal is broken during a patterning step for the source/drain layers.